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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/925,626	08/10/2001	Fumihito Oka	3008-34	2918	
20457 7590 12/17/2003 ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET SUITE 1800			EXAMINER		
			HU, SHOUXIANG		
			ART UNIT	PAPER NUMBER	
ARLINGTON,	, VA 22209-9889	2811	1,		
			DATE MAILED: 12/17/2003	16	

Please find below and/or attached an Office communication concerning this application or proceeding.

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•		Application	No.	Applicant(s)				
Office Action Summary		09/925,626		OKA ET AL.				
		Examiner	·	Art Unit				
		Shouxiang		2811				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
THE I - Exter after - If the - If NO - Failu - Any r	ORTENED STATUTORY PERIOD FOR REF MAILING DATE OF THIS COMMUNICATION isions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a of period for reply is specified above, the maximum statutory perion to teeply within the set or extended period for reply will, by state apply received by the Office later than three months after the mand patent term adjustment. See 37 CFR 1.704(b).	N. 1.1.136(a). In no even reply within the statute iod will apply and will atute, cause the applic	t, however, may a reply be tim ory minimum of thirty (30) days expire SIX (6) MONTHS from t ation to become ABANDONED	ely filed s will be considered timel the mailing date of this c O (35 U.S.C. § 133).	y. ommunication.			
1)🖂	Responsive to communication(s) filed on 06	6 October 2003.						
2a) <u></u> □	This action is FINAL . 2b)⊠ Th	his action is nor	ı-final.					
3) 🗌	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims								
5)□ 6)⊠ 7)□	4) Claim(s) 1-13 and 17-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-13 and 17-20 is/are rejected. 7) Claim(s) is/are objected to.							
•	Claim(s) are subject to restriction and	d/or election red	quirement.					
	on Papers							
9)⊠ The specification is objected to by the Examiner. 10)□ The drawing(s) filed on is/are: a)□ accepted or b)□ objected to by the Examiner.								
10)	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
	Replacement drawing sheet(s) including the corr				FR 1.121(d).			
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority under 35 U.S.C. §§ 119 and 120								
 12)								
Attachmen	t(s)							
2) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s		4) Interview Summary 5) Notice of Informal P 6) Other:					



DETAILED ACTION

Claim Objections

Claims 17-20 are objected to because of the following informalities/defects:
 In claim 17, line 7, the term of "crystallize" should read as: --being crystallized--.
 Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- 3. Claims 1, 3-5, 7 and 17-20, as being best understood in view of the claim rejections above, are rejected under 35 U.S.C. 102(a) as being anticipated by Guliants et al. ("Guliants"; Photovoltaic Specialists Conference, 15-22 September 2000, IEEE, pages 154-157).

Guliants discloses a crystalline silicon thin film semiconductor device that can function as a photovoltaic device (see Fig. 5), comprising: a conductive substrate (Mo; see the first paragraph in the EXPERIMENT section); a crystallographically oriented first polysilicon layer (the lower portion of the MI-grown n-type polysilicon layer; see the bottom-right paragraph at page 156 through the top-left paragraph at page 157); and a second polysilicon layer (the upper portion of the MI-grown n-type polysilicon layer).



wherein the first polysilicon layer is crystallographically oriented and has been formed by introducing a metal catalyst (the Ni film) so as to come in contact with a surface portion of an amorphous silicon layer (the precursor of the lower portion of the MI-grow n-type polysilicon layer) and through heat treatment; the second polysilicon layer inherently has been formed by following the crystallographic orientation of the first polysilicon layer, as the first polysilicon layer is closer to the catalyst film and is crystallized first, which accordingly can inherently function as a seed layer for the crystallization of the second polysilicon layer; and the first and second polysilicon layers have a same first conductivity type (n-type). And, the formation of the second polysilicon layer can be regarded as being without catalytic effect in the sense that the second polysilicon is separated from the metal catalyst by the first polysilicon layer during the formation of the second polysilicon layer. In addition, it is noted that the limitation regarding the second polysilicon layer being formed "without catalytic effect" is a process limitation, which would not carry patentable weight in the claim(s) drawing to a structure, because distinct structure is not necessarily produced. In re Thorpe, 227 USPQ 964, 966 (Fed. Cir. 1985). Furthermore, the limitations regarding how the first polysilicon is formed, more specifically regarding which one of the amorphous silicon layer and the metal catalyst element is in contact with the substrate, as recited in claim 1 (lines 5-6) and claim 17 (lines 3-5), is a process limitation, which would not carry patentable weight in the claim(s) drawing to a structure, because distinct structure is not necessarily produced. In re Thorpe, 227 USPQ 964, 966 (Fed. Cir. 1985). In both cases, there is no metal catalyst remaining between the first and second polysilicon



layers, otherwise, the second polysilicon layer would be formed directly under the catalyst effect.

Regarding claim 3, the second polysilicon layer is formed through columnar growth, which is therefore inherently crystallographically oriented in its thickness direction.

Regarding claims 4 and 7, the thin film semiconductor device of Guliants further includes a third polysilicon layer (the p-type layer in the p-n junction diode structure), which has a same crystallographic orientation as the first and second polysilicon layers.

Regarding claims 17-21, the limitations regarding how the second polysilicon layer is formed, as recited in claims 18-20, are also process limitations, which would not carry patentable weight in the claim(s) drawing to a structure, because distinct structure is not necessarily produced. In re Thorpe, 227 USPQ 964, 966 (Fed. Cir. 1985).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claim 2, as being best understood in view of the claim rejections above, is rejected under 35 U.S.C. 103(a) as being unpatentable over Guliants et al. ("Guliants"; Photovoltaic Specialists Conference, 15-22 September 2000, IEEE, pages 154-157).



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The disclosure of Guliants is discussed as applied to claims 1, 3-5, 7 and 17-20 above.

Guliants further discloses that the second polysilicon layer is formed in a gas mixture comprising hydrogen (see the first paragraph in the EXPERIMENT section).

Although Guliants does not expressly disclose that the second polysilicon layer can contain no less than 0.1% hydrogen, one of ordinary skill in the art would readily recognize that a polysilicon layer can preferably have a hydrogen concentration no less than 0.1% for improving its doping efficiency, as readily evidenced in the prior art such as Saito (US 2002/0011264 A1; see Section [0067]). And, it is further noted that the hydrogen concentration in a polysilicon layer is an art-cognized parameter of importance subject to routine experimentation and optimization.

Therefore, it would have been obvious to one of ordinary skilled in the art at the time the invention was made to make the thin film device of Guliants with the hydrogen concentration in the second polysilicon layer being no less than 0.1%, so that a thin film device with improved doping efficiency and optimized performance would be obtained.

6. Claim 6 and 8-13, as being best understood in view of the claim rejections above, are rejected under 35 U.S.C. 103(a) as being unpatentable over Guliants et al. ("Guliants"; Photovoltaic Specialists Conference, 15-22 September 2000, IEEE, pages 154-157) in view of Okamoto et al. ("Okamoto"; 6,337,224).

The disclosure of Guliants is discussed as applied to claims 1-5, 7 and 17-20 above.



Guliants further discloses that the substrate can also be a glass.

Although Guliants does not expressly disclose that the thin film device can further include a substantially i-type fourth polysilicon layer, and/or a top and/or a bottom electrode, one of ordinary skill in the art would readily recognize that an i-type polysilicon layer can be inserted in the middle to form a p-i-n type photovoltaic device for improving efficiency, and that a top and a bottom electrodes are desirable for reducing the contact resistance of a photovoltaic device, a evidenced in Okamoto (see the i-type layer 205, the top and bottom electrodes 207 and 210 in Fig. 1).

Therefore, it would have been obvious to one of ordinary skilled in the art at the time the invention was made to make the thin film device of Guliants with device further comprising a substantially i-type fourth polysilicon layer and/or a top electrode and/or a bottom electrode, as taught in Okamoto, so that a photovoltaic device with improved efficiency and/or reduced contact resistance would be obtained. And, the fourth polysilicon layer in such a device would inherently have a same crystallographic orientation as the third polysilicon layer, provided it is formed through the same way the third polysilicon layer in Guliants is formed.

Regarding claim 10, one of ordinary skill in the art would readily recognize that a polysilicon layer can preferably have a hydrogen concentration no less than 0.1% for improving its doping efficiency, as readily evidenced in the prior art such as Saito (US 2002/0011264 A1; see Section [0067]). And, it is further noted that the hydrogen concentration in a polysilicon layer is an art-cognized parameter of importance subject to routine experimentation and optimization.



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Therefore, it would have been obvious to one of ordinary skilled in the art at the time the invention was made to make the thin film device collectively taught by Guliants and Okamoto with the hydrogen concentration in the third and the fourth polysilicon layers being no less than 0.1%, so that a thin film device with optimized performance would be obtained.

Response to Arguments

Applicant's arguments filed on 10/06/03 have been fully considered but they are not persuasive.

Applicant's main arguments include: Guliants does not disclose the limitation that the first polysilicon layer is formed by forming the amorphous silicon layer, not the metal catalyst layer, on the substrate. In response, it is noted that the word "on" does not always necessarily have to be interpreted as meaning: on and in direct contact with. Besides, as already explained in the claim rejections set forth above in this Office action, the "forming" limitation is a process limitation, which would not carry patentable weight in the claim(s) drawing to a structure, because distinct structure is not necessarily produced. In re Thorpe, 227 USPQ 964, 966 (Fed. Cir. 1985). In each of cases (both in Guliants and in the instant invention), both of the first and second polysilicon layers (or portions) are crystallographically oriented along a same direction, and there is no metal catalyst element remaining between them. If Applicant believes that a distinct structure feature(s) is/are indeed produced with the recited process limitations, Applicant then should clearly define such distinct structure feature(s) in the



claim(s). And, Applicant is reminded that although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

In addition, it is further noted that, in Guliants, the first polysilicon layer (the lower portion of the MI-grown n-type polysilicon layer; see the bottom-right paragraph at page 156 through the top-left paragraph at page 157) is first crystallographically oriented with the use of a underlying metal catalyst (the Ni film); and the second polysilicon layer (the upper portion of the MI-grown n-type polysilicon layer) is naturally formed by following the crystallographic orientation of the first polysilicon layer without catalytic effect, in the sense that the second polysilicon layer has no direct contact with the metal catalyst as they are separated from each other by the first polysilicon layer.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shouxiang Hu whose telephone number is (703) 306-5729. The examiner can normally be reached on Monday through Thursday, 7:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (703) 308-1690. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9318.



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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

SH

December 11, 2003

SHOUXIANG HU PRIMARY EXAMINER

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